

35327 U.S. PTO
10/026551

12/27/01

7B

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

| | | | | | |
|--|-------------|--|----------|--------------------|-----------|
| APPL NUM | FILING DATE | CLASS | SUBCLASS | GAU | EXAMINER |
| 10026551 | 12/27/2001 | 438 | 14 | 2812 | Stevenson |
| **APPLICANTS: Hosokawa Tomohiro; Shimizu Satoshi; | | | | | |
| **CONTINUING DATA VERIFIED: | | | | | |
| ** FOREIGN APPLICATIONS VERIFIED: JAPAN 2000-397501 12/27/2000 | | | | | |
| PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> | | RESCIND <input type="checkbox"/> | | | |
| Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no | | 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no | | ATTORNEY DOCKET NO | |
| Verified and Acknowledged Examiners's initials | | 50006-135 | | | |
| TITLE : Method of manufacturing semiconductor device and system for manufacturing the same | | | | | |
| U.S. DEPT. OF COMM./PAT. & TM-PTO-435L (Rev. 12-94) | | | | | |

| | | | | | |
|--|-----------|---------------------------|--|-----------------------------|----------------------|
| NOTICE OF ALLOWANCE MAILED | | Assistant Examiner | | CLAIMS ALLOWED | |
| | | | | Total Claims | Print Claim for O.G. |
| ISSUE FEE | | Primary Examiner | | DRAWING | |
| Amount Due | Date Paid | | | Sheets Drwg. | Figs.Drwg. |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | PREPARED FOR ISSUE | | Application Examiner | |
| WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only. | | | | | |

FILED WITH: ☐ DISK (CRF) ☐ CD-ROM
(Attached in pocket on right inside flap)